

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

2

Attorney Docket Number

X-805-6 US

Complete if Known

Application Number

Not Yet Known

Filing Date

November 28, 2000

First Named Inventor

Raymond C. Pang

Group Art Unit

Not Yet Known

Examiner Name

Not Yet Known

Attorney Docket Number

X-805-6 US

UC796 U.S. PTO
09/724975

11/28/00

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

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Date Considered

5/28/04

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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08A (10-96)
Approved for use through 10/31/99. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known

Application Number	Not Yet Known
Filing Date	November 28, 2000
First Named Inventor	Raymond C. Pang
Group Art Unit	Not Yet Known
Examiner Name	Not Yet Known
Attorney Docket Number	X-805-6 US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
mon		Xilinx Application Note, "Configuration Issues: Power-Up, Volatility, Security, Battery Back-Up", XAPP 092, November 24, 1997 (Version 1.1).	
		Virtex Configuration Guide, published by Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124, October 9, 2000 (Version 1.0).	
		Bruce Schneier "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Second Edition, Copyright 1996, published by John Wiley & Sons, Inc.; Chapter 9, pages 193-194 and 200-203; Chapter 12, pages 265-301 and Chapter 15, pages 360-361	
		Cahners EDN ACCESS Web Page, "Cunning circuits confound crooks," October 12, 2000; pages 1-8; http://www.ednmag.com/ednmag/reg/2000/10122000/21df2.htm	
		Xilinx, Application Note, XAPP138, "Virtex FPGA Series Configuration and Readback", published October 4, 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124.	

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Signature

[Handwritten Signature]

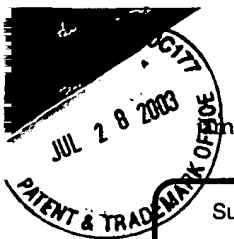
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**INFORMATION DISCLOSURE
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Application / Conf. No.	09/724,975 / 7824
Filing Date	November 28, 2000
First Named Inventor	Raymond C. Pang
Art Unit	2818
Examiner Name	Unknown
Attorney Docket Number	X-805-6 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
mon ↓		US- US2001/0015919	08-23-01	Kean	
		US- 5,970,142	10-19-99	Erickson	
		US- 5,615,263	03-25-97	Takahashi	
		US-			
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FOREIGN PATENT DOCUMENTS

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mon		WO 00/49717	08-24-00	Trimberger		

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